

FIG. 1C

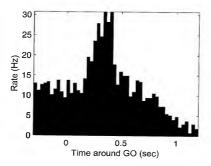
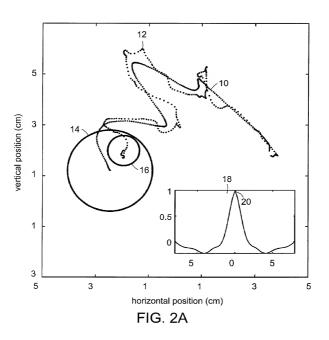


FIG. 1D



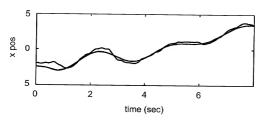


FIG. 2B

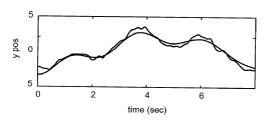


FIG. 2C

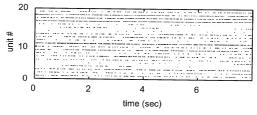


FIG. 2D



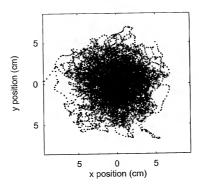


FIG. 3A

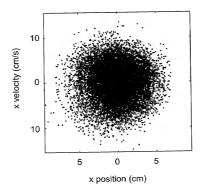


FIG. 3B

A gray A

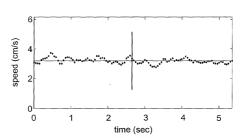


FIG. 3C

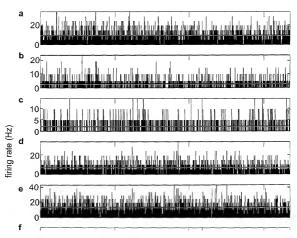
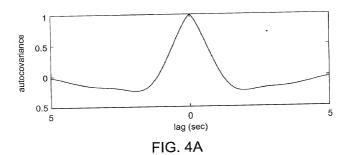


FIG. 3D



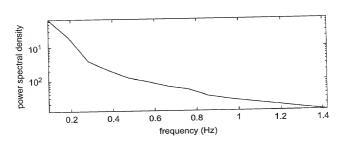


FIG. 4B

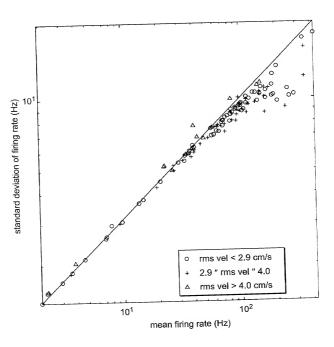


FIG. 5

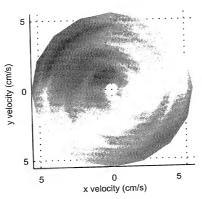


FIG. 6A-1

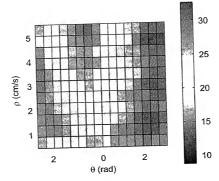


FIG. 6A-2

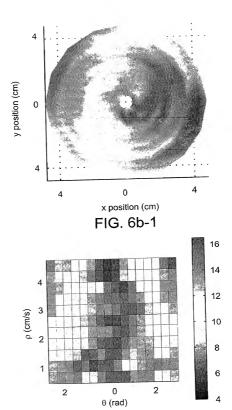


FIG. 6B-2

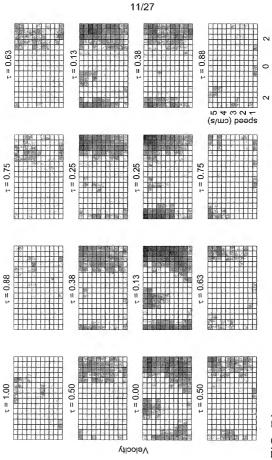
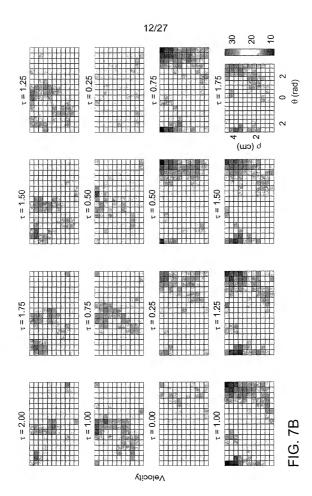
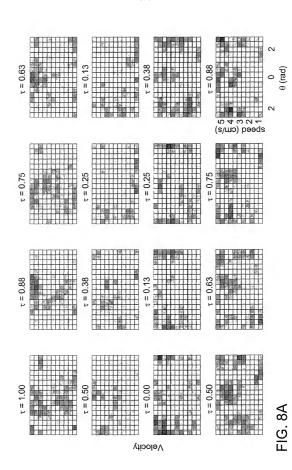
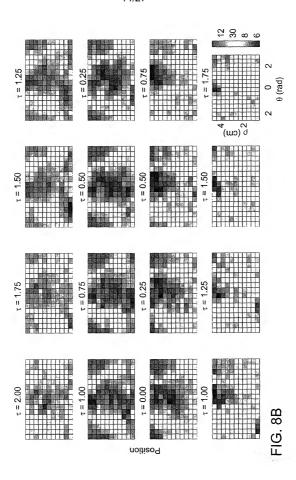
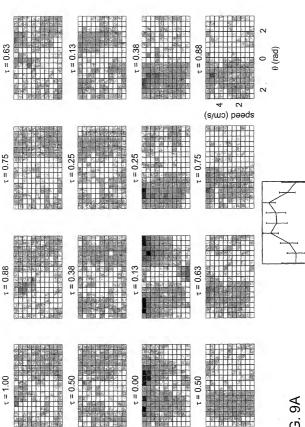


FIG. 7A









Velocity

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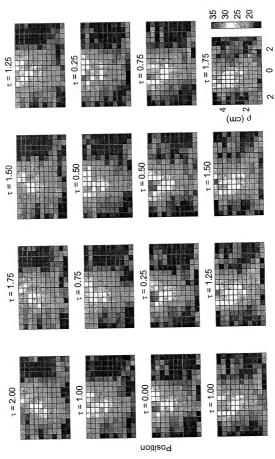
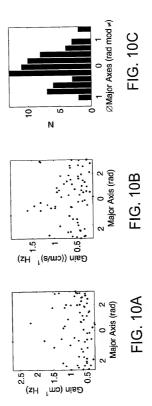
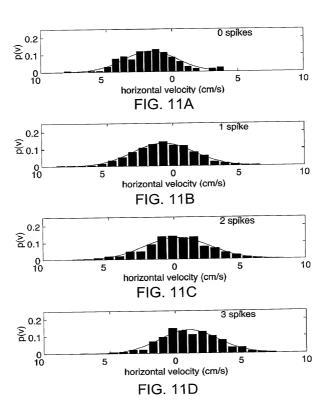
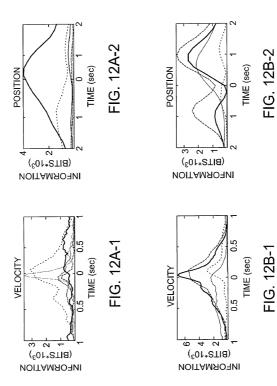


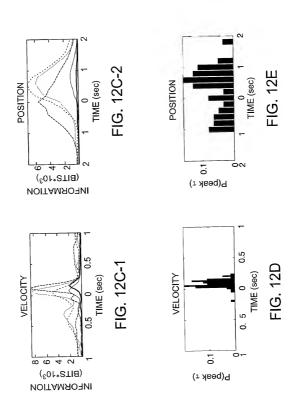
FIG. 9B

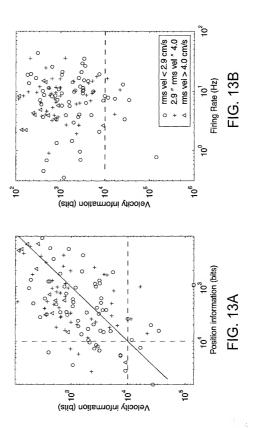


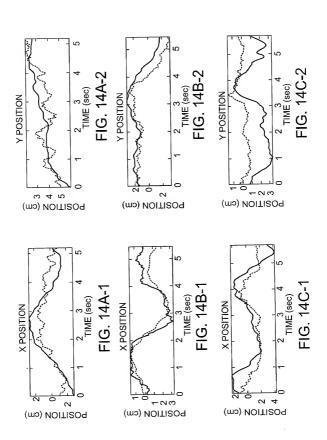


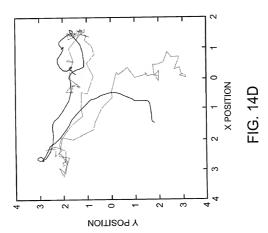


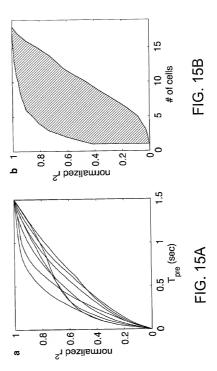
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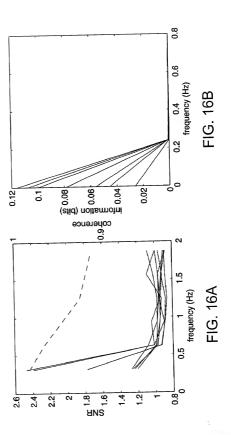












DESIGN FOR FULLY IMPLANTABLE, TELEMETRIC ELECTRODE ARRAY SYSTEM

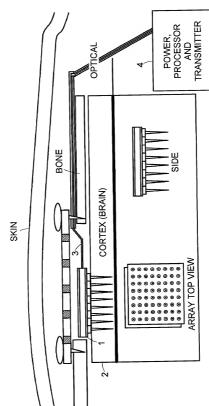


FIG. 17

FIG. 19